

# PT32F030 Series

## SELECTION GUIDE



Part Number	Memory		Core	CPU Frequency	Operating Voltage	GPIO	DMA	Timer & Counter				Communication Interface			Clock				Analog				Security	Algorithm	Operating Temperature	Package Information			
	Flash (K)	RAM (K)		MHz	VDD (V)			I/O	Adv	Gen	Base	WDG	UART	I2C	SPI	HSE (MHz)	HSI (MHz)	LSI (KHz)	PLL (mult)	ADC 12-bit	COMP	OPA	PVD		Beep	CRC	°C	Package Types	Pin Pitch
PT32F030G6P7*	32	4	M0	72	2.2-5.5	24	2	1	3	2	1	2	2	2	4~25	36	32	2	16ext+2int	3	2	√	√	√	MULT. DIV, MAC, SQRT, CORDIC	-40-105	TSSOP28	0.65mm	Tube
PT32F030G8P7	64	8	M0	72	2.2-5.5	24	2	1	3	2	1	2	2	2	4~25	36	32	2	16ext+2int	3	2	√	√	√	MULT. DIV, MAC, SQRT, CORDIC	-40-105	TSSOP28	0.65mm	Tube
PT32F030K6U7*	32	4	M0	72	2.2-5.5	26	2	1	3	2	1	2	2	2	4~25	36	32	2	16ext+2int	3	2	√	√	√	MULT. DIV, MAC, SQRT, CORDIC	-40-105	QFN32 (5*5)	0.5mm	Tray
PT32F030K8U7	64	8	M0	72	2.2-5.5	26	2	1	3	2	1	2	2	2	4~25	36	32	2	16ext+2int	3	2	√	√	√	MULT. DIV, MAC, SQRT, CORDIC	-40-105	QFN32 (5*5)	0.5mm	Tray
PT32F030K6T7*	32	4	M0	72	2.2-5.5	26	2	1	3	2	1	2	2	2	4~25	36	32	2	16ext+2int	3	2	√	√	√	MULT. DIV, MAC, SQRT, CORDIC	-40-105	LQFP32 (7*7)	0.8mm	Tray
PT32F030K8T7	64	8	M0	72	2.2-5.5	26	2	1	3	2	1	2	2	2	4~25	36	32	2	16ext+2int	3	2	√	√	√	MULT. DIV, MAC, SQRT, CORDIC	-40-105	LQFP32 (7*7)	0.8mm	Tray
PT32F030C6T7*	32	4	M0	72	2.2-5.5	40	2	1	3	2	1	2	2	2	4~25	36	32	2	16ext+2int	3	2	√	√	√	MULT. DIV, MAC, SQRT, CORDIC	-40-105	LQFP48 (7*7)	0.5mm	Tray
PT32F030C8T7	64	8	M0	72	2.2-5.5	40	2	1	3	2	1	2	2	2	4~25	36	32	2	16ext+2int	3	2	√	√	√	MULT. DIV, MAC, SQRT, CORDIC	-40-105	LQFP48 (7*7)	0.5mm	Tray

\* Under Development